Amendments to the Specification:

Please add the following new paragraph after the title:

CROSS-REFERENCE TO RELATED APPLICATION

This application is a divisional of co-pending United States Patent Application Serial no. 09/929,652, entitled "Method and Apparatus for Trimming High-Resolution Digital-To-Analog Converter", filed on August 13, 2001, in the name of the same inventors and commonly owned herewith.

Amendments to the Specification:

Please replace paragraph [0019] with the following amended paragraph:

[0019]   FIG. 4B illustrates a modification of the FIG. 4A embodiment. In this version trimmable current source 62b is modified from trimmable current source 62a of FIG. 4A by omitting current-source transistor M1. Thus, all current in this version is sourced by the trim device 64b.[[,]]

Please replace paragraph [0032] with the following amended paragraph:

[0032]   Alternatively, as illustrated in FIG. 6B, the comparator 83 is able to compare two consecutively received values and therefore has only one input coupled to the current output of the DAC. To trim Bit 1 to match its current to that of Bit 0, first input codeword 0000 (using thermometer decoding, a "1" connects the current source to the output and a "0" disconnects it)

<u>and</u> [[nd]] turn on Bit 0. Comparator 83 senses the output and uses it as a reference. <u>Then</u> [[The]] input codeword 0001 and turn off Bit 0. Comparator 83 now determines whether Bit 1 matched Bot 0. If not, Bit 1 is trimmed and the procedure is repeated until they match.